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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/626,574	07/27/2000	Michael R. May	SIG000049	9605	
759	90 01/28/2003				
GARLICK, HARRISON & MARKISON LLP			EXAMINER		
	P. O. BOX 160727 AUȘTIN, TX 78716			LUU, AN T	
			ART UNIT	PAPER NUMBER	
			2816		
			DATE MAILED: 01/28/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

		<u>Um</u>
•	Application No.	Applicant(s)
	09/626,574	MAY, MICHAEL R.
Office Action Summary	Examiner	Art Unit
	An T. Luu	2816
Th MAILING DATE of this communication app Period for Reply	ars on the cover shet with th	e corr spond nce address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	36(a). In no event, however, may a reply be within the statutory minimum of thirty (30) vill apply and will expire SIX (6) MONTHS for cause the application to become ABANDO	e timely filed days will be considered timely. rom the mailing date of this communication. DNED (35 U.S.C. § 133).
1) Responsive to communication(s) filed on 10 L	December 2002 .	
2a)⊠ This action is FINAL . 2b)□ Thi	is action is non-final.	
3) Since this application is in condition for allowa closed in accordance with the practice under		
Disposition of Claims 4) Claim(s) 1-10 is/are pending in the application		
4) Claim(s) 1-19 is/are pending in the application4a) Of the above claim(s) is/are withdraw		
5) Claim(s) is/are allowed.	vii itotii consideration.	
6)⊠ Claim(s) <u>1-6,9,11-15,17-18</u> is/are rejected.		
7)⊠ Claim(s) <u>7.8,10 and 16</u> is/are objected to.		
8) Claim(s) 7.0, 10 and 10 is/are objected to:	r election requirement	
Application Papers	r ciconon requirement.	
9) The specification is objected to by the Examine	r.	
10) The drawing(s) filed on is/are: a) □ accep	oted or b)□ objected to by the E	xaminer.
Applicant may not request that any objection to the	e drawing(s) be held in abeyance.	See 37 CFR 1.85(a).
11)☐ The proposed drawing correction filed on	_is: a)□ approved b)□ disap	proved by the Examiner.
If approved, corrected drawings are required in rep	bly to this Office action.	
12) The oath or declaration is objected to by the Ex	aminer.	
Priority under 35 U.S.C. §§ 119 and 120		
13) Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 11	9(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:		
1. Certified copies of the priority documents	s have been received.	
2. Certified copies of the priority documents	s have been received in Applic	cation No
 3. Copies of the certified copies of the prior application from the International But * See the attached detailed Office action for a list 	reau (PCT Rule 17.2(a)).	•
14) Acknowledgment is made of a claim for domestic	c priority under 35 U.S.C. § 11	9(e) (to a provisional application).
a) ☐ The translation of the foreign language pro 15)☐ Acknowledgment is made of a claim for domesti		
Attachment(s)		
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Inform	nary (PTO-413) Paper No(s) nal Patent Application (PTO-152)

DETAILED ACTION

1. Applicant's Response filed on 12-10-2002 has been received and entered in the case. The rejections set forth in the previous Office Action are maintained as indicated below.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1, 6, 9, 11 and 15 are rejected under 35 U.S.C. 102(b) as being anticipated by the Farrell et al. reference (U.S. Patent 5,510,740).

The Farrell et al discloses in figure 8 an apparatus comprising a filter 820 received an input signal (reset signal) 802 via a processing element 816 for producing a pulse signal 826 and a latch 824 coupled to the filter for latching the pulse signal as claimed in claims 1, 6, 9 and 15. It is noted that signal 802 is received from a terminal which is an input gating device.

As to claim 11, it is inherent that there are two modes for a gating device (i.e., ON/OFF).

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

5. Claims 2-3 and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over the Farrell et al. reference (U.S. Patent 5,510,740) in view of the Tsukikawa reference (U.S. Patent 6,121,812).

The Farrell et al discloses all the claimed invention except for having a latch element comprising specific components being configured as claimed. Tsukikawa discloses in figure 8 a latch circuit 30 comprising a first inverter 42 and a second inverting logic element (NAND gate 43) being configured as recited in claims 2-3. It would have been obvious to one skilled in the art to replace a generic latch circuit disclosed by Farrell by the one taught by Tsukikawa because the skilled artisan in the art would easily recognize that a latch circuit can be implemented in many different ways in the art without patentable distinction. The selection among different designs of a latch is seen as a design expedient dependent upon the particular requirement of the application.

6. Claims 4-5, 13-14 and 17-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over the Farrell et al. reference (U.S. Patent 5,510,740) in view of the Okada reference (U.S. Patent 4,306,198).

The Farrell et al discloses all the claimed invention except for having a filter element comprising specific components being configured as claimed. Okada discloses in figure3 an apparatus comprising a capacitor C coupled to the input T1 and a gating circuit, including a controlled impedance Q5, Q6, coupled to the capacitor such that the capacitor and an impedance of at least one element of the gating circuit are tuned based on the rise time and fall time of the input signal as required by claims 4-5 and 13-14. It is noted that an inverter is not required since the output of the inverter is connected to a drive transistor Q1, if the complementary type of Q1

is used instead. It would have been obvious to one skilled in the art to replace a generic filter circuit disclosed by Farrell by the one taught by Okada because the skilled artisan in the art would easily recognize that a filter circuit can be implemented in many different ways in the art without patentable distinction. The selection among different designs of a filter is seen as a design expedient dependent upon the particular requirement of the application.

As to claim 17, the scope of this claim is similar to the combination scope of claims 9 and 13-14. Thus, it is rejected for the same reasons set forth above.

As to claim 18, the scope of this claim is similar to that of claims 2 or 12. Thus, it is rejected for the same reason(s) set forth above.

Response to Arguments

7. Applicant's arguments filed 12-10-02 have been fully considered but they are not persuasive.

Regarding to rejection of claims 1, 6, 9, 11 and 15 under 35 USC 102(b), Applicant argued that the prior art (Farrell) does not teach or suggest the limitation "the filter module produces a **pulse** signal in response to an **edge** of the input logic signal" and Farrell teaches away from claim 1 because "the output of reset filter 820 is only applied to the reset leading edge detector 808 when the reset signal of reset signal of reset input line 802 is of a predetermined time duration because of the action of reset filter 820". And Applicant has concluded that "Farrell does not teach or suggest a filter module that produces a pulse signal in response to an edge of an input logic signal, as claimed by Applicant". Examiner respectfully disagrees with Applicant because figure 9 and associated description (col. 22, lines 20-24) disclose a pulse as

shown on diagram line 920 and this pulse does not respond instantly at the rising edge of the reset signal. Rather, it is responded to the rising edge of the reset signal after a predetermined time (i.e., period 916). Therefore, Examiner believes that limitation "the filter module produces a pulse signal in response to an edge of the input logic signal" is anticipated by Farrell.

With respect to term "comprises", Examiner respectfully remind Applicant that definition for this term is given in MPEP Sec. 2111.03). Based on the Applicant's example given on page 4, Examiner respectfully believes that Applicant does not interpreted this term according to MPEP. Further, Examiner is not required to interpret claims of patent application in view of the specification as suggested by Applicant. (MPEP 2111.01)

Regarding to rejection of claims 2, 3, 7 and 12 under 35 USC 103(a) by Farrell in view of Tsukikawa. Applicant has argued that the independent claims (1 and 9) do not read on Farrell. Therefore, their dependent claims (2, 3 and 12) cannot be rendered obvious by Farrell in view of Tsukikawa. Examiner respectfully disagrees because claims 1 and 9 do read on Farrell as noted above.

Regarding to rejection of claims 4, 5, 13, 14, 17 and 18 under 35 USC 103(a) by Farrell in view of Okada. Applicant has argued that the independent claims (1 and 9) do not read on Farrell and it is not obvious to combine references to reject his dependent claims. Examiner's response noted above is applicable here.

It is noted that motivation is provided for each of the rejections of claims under 35 USC 103.

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Allowable Subject Matter

8. Claims 7, 8, 10 and 16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

9. The following is a statement of reasons for the indication of allowable subject matter: the prior art of record fails to disclose a second filter being structurally configured in the claimed apparatus as recited.

Conclusion

10. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

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11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to An T. Luu whose telephone number is 703-308-4922. The examiner can normally be reached on 7:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on 703-308-4876. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

An T. Luu 1-24-03

JMOTHY P. CALLAHAN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800